



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No. 6,744,102 B2
Patent Issue Date June 1, 2004
Application Serial No. 10/087,416
Filing Date February 27, 2002
Assignee Micron Technology, Inc.
Inventorship Trivedi et al.
Attorney's Docket No. MI22-1965
Title: MOS Transistors with Nitrogen in the Gate Oxide of the P-Channel
Transistor

REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT
FOR PTO MISTAKE (37 C.F.R. 1.322(a))

To: Commissioner for Patents Certificate
P.O. Box 1450 DEC 02 2004
Alexandria, VA 22313-1450
ATTN: Decision and Certificate of Correction
Branch of the Patent Issue Division of Correction
From: D. Brent Kenady (Tel. 509-624-4276; Fax 509-838-3424)
Wells St. John P.S.
601 W. First Avenue, Suite 1300
Spokane, WA 99201-3828

Sir:

It is hereby requested that a Certificate of Correction be issued with respect to Patent No. 6,744,102 B2, granted June 1, 2004, in accordance with the Certificate of Correction form attached hereto in duplicate.

The errors listed on the Certificate of Correction form were apparently incurred through the fault of the PTO as will be disclosed by the records of files in the Office.

Since this Certificate of Correction is being requested due to PTO errors, it is believed that no fee is due. However, in the event that a fee is required for issuance of this Certificate of Correction, please charge the fee specified under

37 C.F.R. § 1.20(a) to Deposit Account No. 23-0925. Please credit Deposit Account No. 23-0925 with any overpayment of the above fee.

Attached hereto, in duplicate is Form PTO-1050, with at least one copy being suitable for printing.

Respectfully submitted,

Dated: 11-22-04

By:


D. Brent Kenady
Reg. No. 40,045

DEC 06 2004

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,744,102 B2

DATED : June 1, 2004

INVENTOR(S) : Trivedi et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 6, lines 21-22, please delete "sui-cides" after "metal" and insert --silicides--.

Col. 8, line 34, claim 17, please insert --silicon-- after "comprising".

Col. 8, line 38, claim 18, please delete "of circuitry" after "circuitry".

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1 of 1

Mailing Address of Sender: D. Brent Kenady Wells St. John P.S. 601 West First Avenue, Suite 1300 Spokane, WA 99201-3828	Patent No. <u>6,744,102 B2</u>
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UNITED STATES PATENT AND TRADEMARK OFFICE
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PTO/SB/21 (03-03)

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U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE*Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.***TRANSMITTAL
FORM***(to be used for all correspondence after initial filing)*

		Application Number	10/087,416
		Filing Date	Feb. 27, 2002
		First Named Inventor	Trivedi et al.
		Art Unit	2811
		Examiner Name	D. Owens
Total Number of Pages in This Submission		Attorney Docket Number	MI22-1965

ENCLOSURES (Check all that apply)

<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> After Allowance Communication to a Technology Center (TC)
<input type="checkbox"/> Fee Attached	<input type="checkbox"/> Licensing-related Papers	<input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences
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<input type="checkbox"/> Extension of Time Request	<input type="checkbox"/> Change of Correspondence Address	<input checked="" type="checkbox"/> Other Enclosure(s) (please identify below):
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<input type="checkbox"/> Response to Missing Parts/ Incomplete Application	Remarks	
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual	D. Brent Kenady, Reg. No. 40,045 Wells St. John, P.S.	
Signature		
Date	11-22-04	

CERTIFICATE OF TRANSMISSION/MAILING

I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231 on this date:

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